**CSL862 Minor 2**

Answer all 7 questions 13/10/2011 Max Marks: 25

1. **ExitLess Interrupts**
2. Throughput Improvements: Explain the following figure (Figure 5 from the ELI paper) [3 marks]



1. Security: Using ELI, can a malicious guest disable interrupts forever? If so, how does ELI recover from this situation. [1]
2. ELI grants the guest direct access to the EOI (end-of-interrupt) register. What can happen if the guest exits to the host without signaling the completion of in-service interrupts? [2]
3. **vIC: Interrupt Coalescing for vStorage**
	1. Many important applications issue synchronous IOs. Delaying the completion of prior IOs can delay issue of future ones. How does vIC ensure that coalescing in this case does not decrease throughput? [2]
	2. How does vIC upper-bound the average IO latency while coalescing? [2]
4. **Protection Strategies for Direct Access to Virtualized I/O Devices**
5. In Table 1, why doesn’t the software-only approach (last line) not protect against bad device access for inter-guest protection? [1]
6. In Table 3, why is the CPU utilization for implementing protection highest in the “Single-Use” strategy. [2]
7. **vIOMMU**
8. True/False: Direct device assignment (without vIOMMU) disallows memory overcommitment causing low consolidation ratios. Explain. [2]
9. For sidecore emulation to be possible, the paper suggests that the device must follow a synchronous register write protocol. Why is this required? [2]
10. **Uniprocessor Record/Replay**

Arrange the following in increasing order of Uniprocessor Record/Replay overhead. If you expect two workloads to have the same overhead, indicate that using an “=” sign. For example, A>B=C>D

1. Busyloop
2. User-level Memsweep on Linux
3. Kernel-level Memsweep on Linux
4. “dd” utility to copy one file to another file inside the same ext3 filesystem on fully-replayed disk
5. “dd” utility to copy one file to another file inside the same ext3 filesystem on output-replayed disk

[2 marks]

1. **Multiprocessor Record/Replay [3]**
2. True/False: When using SMP-Revirt, one should prefer using blocking locks than spin locks. Briefly explain. [1]
3. Write a program where using SMP-Revirt on two CPUs will be slower than running the program on one CPU and using uniprocessor Record/Replay. [2]
4. **DoublePlay**
5. If DoublePlay did not record the order of synchronization operations, would the system still work? What could be the problem? [1]
6. What is forward recovery (in case of a conflict) and why is it a good idea? What are the implementation complications and how are they resolved? [2]